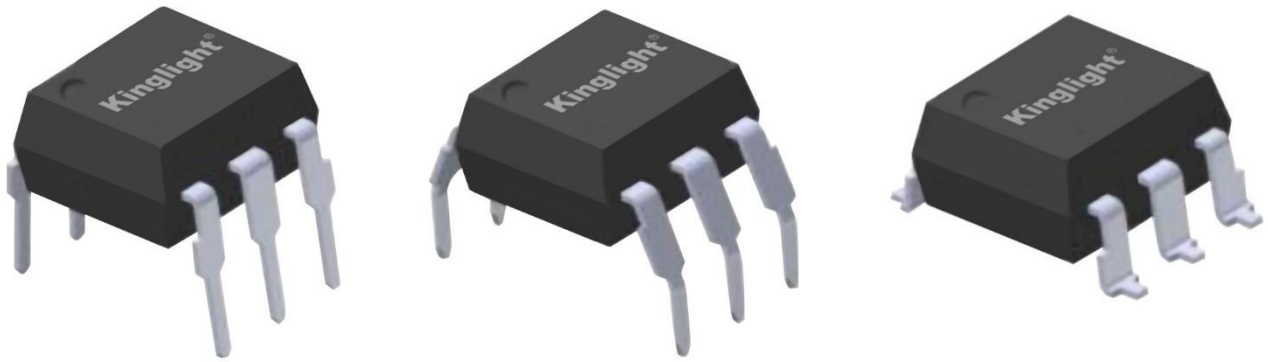
Kinglight®
晶台微电子

KL305X
RANDOM-PHASE TRIAC DRIVER PHOTOCOUPLER

DIP6 随机相位双向可控硅驱动光电耦合器(消费级)



* 本文档中包含的信息反映了具有代表性的使用场景，仅供技术参考。
  The information contained in this document reflects representative usage scenarios and is intended for technical reference only.
* 本文档中提到的产品型号和规格如有更改或改进，恕不另行通知。在生产使用之前，客户应参考产品规格书的最新数据表。
  Product models and specifications mentioned in this document are subject to change or improvement without notice. Customers should refer to the latest data sheets in the product specifications prior to production use.
* 在使用本文档中引用的产品时，请确保产品在数据手册中规定的环境和电气限制范围内运行。如果客户使用超过指定的限制，晶台微电子将不会对任何后续问题负责。
  When using the products referenced in this document, ensure that the products are operated within the environmental and electrical limits specified in the data sheet. If the customer uses the product beyond the specified limits, Kinglight Microelectronics will not be responsible for any subsequent problems.
* 本文档中的信息适用于电子元器件应用中的典型用法。如有任何特殊用途，请向晶台微电子咨询，以获得进一步的帮助。
  The information in this document applies to typical use in electronic component applications. For special applications, please contact Kinglight Microelectronics for further assistance.
* 未经晶台微电子允许，不得复制或转载本文件的内容和信息。对于最新的信息，请参考官方网站 Http://www.klmicro.com。
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1. 产品特点 Product features

- 峰值击穿电压 Peak breakdown voltage
  KL305X: 600V
- 输入与输出间高隔离电压(Viso=5000 V rms)
  High isolation voltage between input and output (Viso=5000 V rms)
- 紧凑型双列直插式封装 Compact dual-in-line package
- 符合欧盟REACH法规 Compliance with EU REACH
- 无Pb且符合ROHS标准 Pb free and RoHS compliant
- 安全审批 Safety approval
  CQC认证已批准(编号:CQC23001407999) CQC approved (No.CQC23001407999)
  UL认证已批准(编号:UL-CA-2340753-0) UL approved (No. UL-CA-2340753-0)
  VDE认证已批准(编号:40059021) VDE approved (No.40059021)

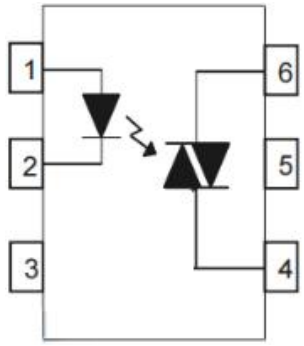
2. 产品描述 Product Description

- KL305X由一个砷化镓红外发光二极管和一个单晶硅芯片的随机相位光电双向晶闸管组成的可控硅光电耦合器
  Each KL305X device consists of a GaAs infrared emitting diode optically coupled to a monolithic silicon random phase photo Triac
- 它被设计用于电子控制和电源试验之间的接口, 以控制115到240V AC操作的电阻和感应负载
  It is designed for interfacing between electronic controls and power triacs to control resistive and inductive loads for 115 to 240 VAC operations.

3. 产品应用 Product Applications

- 电磁阀控制、温度控制、电机控制
  Solenoid/valve controls, Temperature controls, Motor controls
- 静态交流电源开关 Static AC power switch
- 微处理器与115至240V AC外围设备接口 Interfacing microprocessors to 115 to 240Vac peripherals
- 白炽灯调光器、灯整流器 Incandescent lamp dimmers, Lamp ballasts

4. 功能图 Functional Diagram



引脚配置 Pin Configuration
1. 阳极Anode
2. 阴极Cathode
3. 无连线No Connection
4. 终端Terminal
5. 基板(不接线)Substrate (do not connect)
6. 终端Terminal

5. 光电特性 Electrical-Optical characteristics

• 最大限度额定值(温度=25℃) Absolute Maximum Ratings(Ta=25℃)

参数 Parameter		符号 Symbol	额定值 Rated Value	单位 Unit
输入 Input	正向电流 Forward current	I_F	60	mA
	反向电压 Reverse voltage	V_R	6	V
	功耗Power dissipation	P_D	100	mW
	降额系数(高于Ta=85℃) Derating factor (above Ta = 85°C)		3.8	mW /℃
输出 Output	断态输出端电压 Off-state Output Terminal Voltage	V_{DRM}	600	V
	峰值重复浪涌电流 Peak Repetitive Surge Current (pw=100μs,120pps)	I_{TSM}	1	A
	导通有效值电流 On-State RMS Current	$I_{T(RMS)}$	100	mA
	输出功耗 Output Power dissipation	Pc	300	mW
	降额系数(高于Ta=85℃) Derating factor (above Ta = 85°C)		7.4	mW/℃
总消耗功率 Total Consume Power		P_{TOT}	330	mW
隔离电压 (1*) Isolation Voltage		V_{iso}	5000	Vrms
工作温度 Operating temperature		T_{OPR}	-55 to +100	℃
储存温度 Storage temperature		T_{STG}	-55 to +125	℃
焊接温度 (2*) Soldering temperature		T_{SOL}	260	℃

附注 (Notes):

1* 交流电源1分钟内, 相对湿度在40~60%RH环境下, 隔离电压测试时, 1&2&3脚短接在一起, 4&5&6脚短接在一起
AC for 1 minute, R.H.= 40 ~ 60% R.H. In this test, pins 1, 2&3 are shorted together, and pins 4, 5 & 6 are shorted together.

2* 焊接时间为10秒 Soldering time is 10 seconds

6. 电气特性(Ta=25℃,除非另有规定)

Electrical Characteristics(Ta=25℃ unless specified otherwise)

参数 Parameter		符号 Symbol	最小值 Min.	规格值 Typ.	最大值 Max.	单位 Unit	条件 Condition
输入 Input	正向电压 Forward voltage	V_F	-	1.18	1.5	V	I_F=10mA
	反向电流 Reverse current	I_R	-	-	10	μA	V_R=6V
输出 Output	断态峰值电流 Peak Blocking Current	I_{DRM}	-	-	100	nA	V_{DRM} = Rated V_{DRM} I_F = 0 mA 2*
	峰值导通电压 Peak On-state Voltage	V_{TM}	-	-	2.5	V	ITM=100 mA peak, I_F=Rated I_{FT}
	断态电压临界上升率 Critical Rate of Rise off-state Voltage	dv/dt	1000	-	-	V/μs	V_{PEAK} =400V, I_F=0mA

• 附注(Notes):

1*. Ta=25℃时的规格值 Typical values at Ta = 25°C

2*. 测试电压必须在dv/dt额定值范围内 Test voltage must be applied within dv/dt rating

3*. 这是静态的dv/dt，转换直流dv/dt只有负载驱动晶闸管的函数

This is static dv/dt,Commutating dv/dt is a function of the load-driving thyristor(s) only

• 传输特性 (Ta=25℃, 除非另有规定)
Transfer Characteristics (Ta=25℃ unless specified otherwise)

参数 Parameter		符号 Symbol	最小值 Min.	规格值 Typ.	最大值 Max.	单位 Unit	条件 Condition
LED触发电流 LED Trigger Current	KL3051	I_{FT}	-	-	15	mA	主端子电压=3V Main terminal Voltage=3V 4*
	KL3052	I_{FT}	-	-	10		
	KL3053	I_{FT}	-	-	5		
保持电流 Holding Current		I_H	-	250	-	uA	

附注(Notes):

4*. 所有设备都保证在小于或等于最大IFT的IF值触发. 因此，建议操作IF在最大IFT(KL3051 15mA、KL3052 10mA、KL3053 5mA)和绝对最大IF(60mA)之间
All devices are guaranteed to trigger at an IF value less than or equal to max IFT. Therefore, Recommended operating IF lies between max IFT (15 mA for KL3051,10 mA forKL3052, 5 mA for KL3053) and absolute maximum IF (60 mA).

7. 特性曲线 Characteristic Curves

图1. 正向电流与正向电压的关系
Forward Current VS Forward Voltage

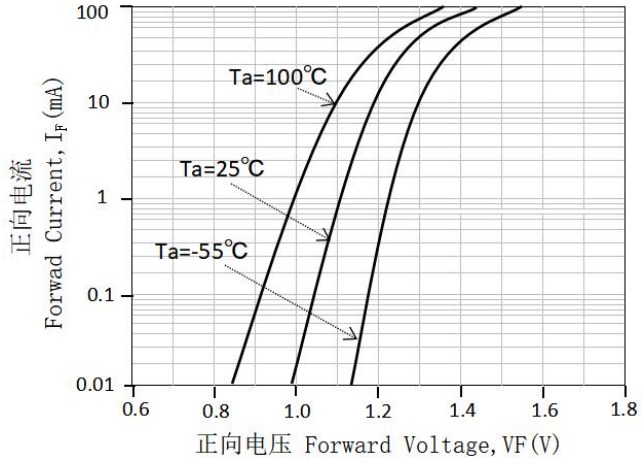


图2. 导通特性
On-State Characteristics

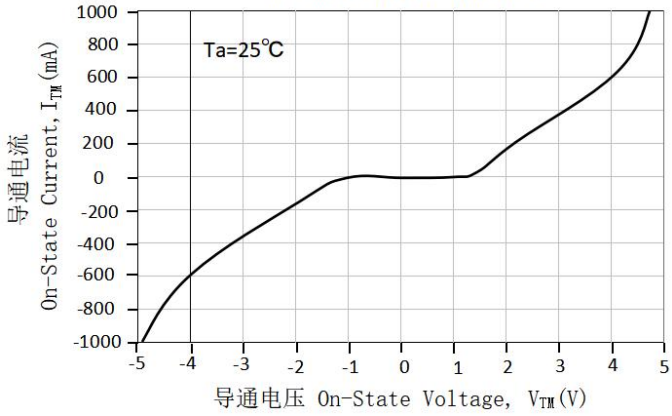


图3. 保持电流 vs 环境温度的关系
Holding Current vs Ambient Temperature

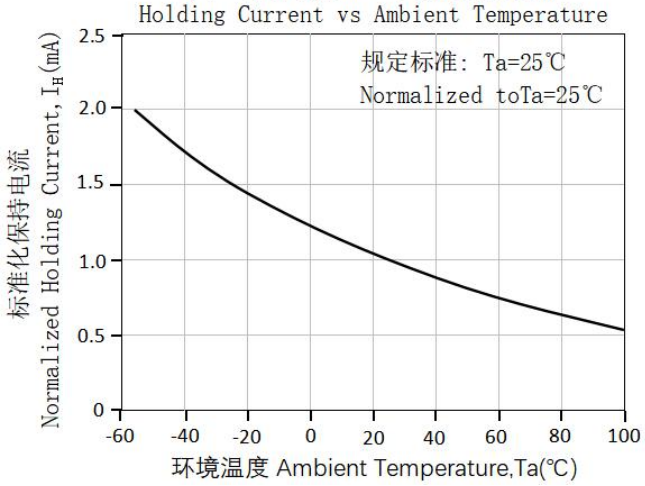


图4. 触发所需的LED电流 vs LED脉冲宽度的关系
LED Current Required to Trigger vs LED Pulse Widt

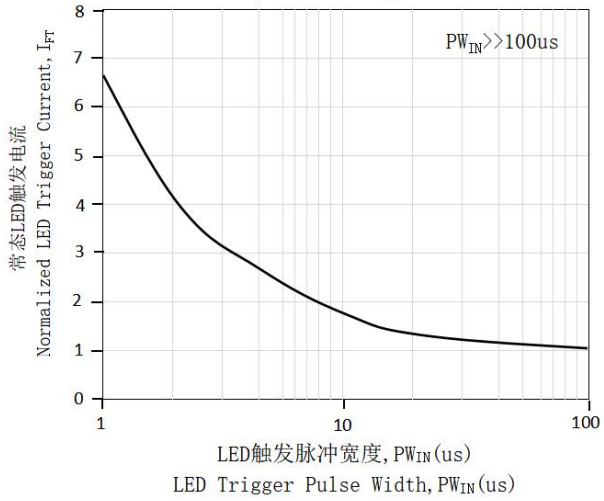


图5. 漏电流 vs 环境温度的关系
Leakage Current VS Ambient Temperature

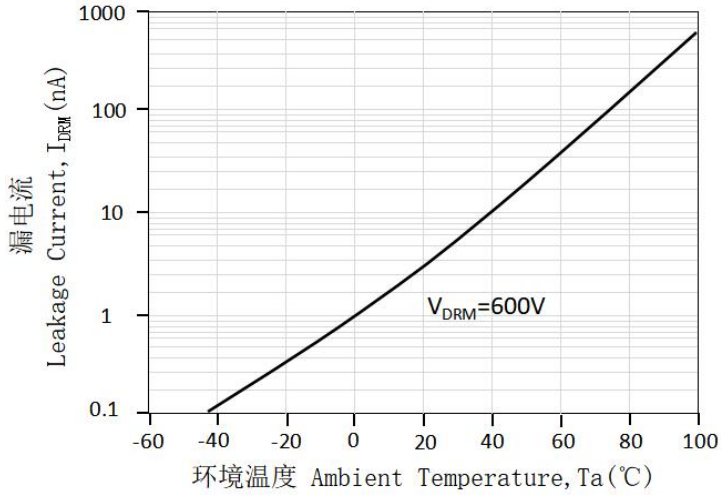


图6. LED触发电流 vs 环境温度的关系
LDE Trigger Current vs Ambient Temperature

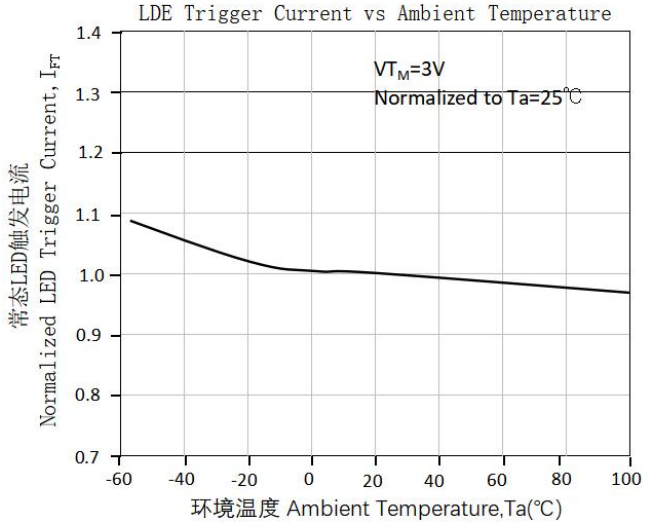


图7. 关闭状态输出端电压vs环境温度的关系
Off-State Output Terminal Voltage vs Ambient Temperature

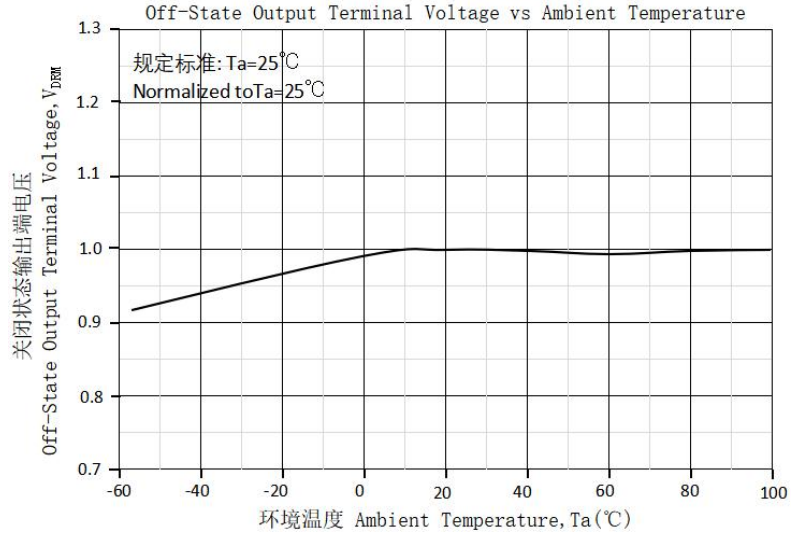
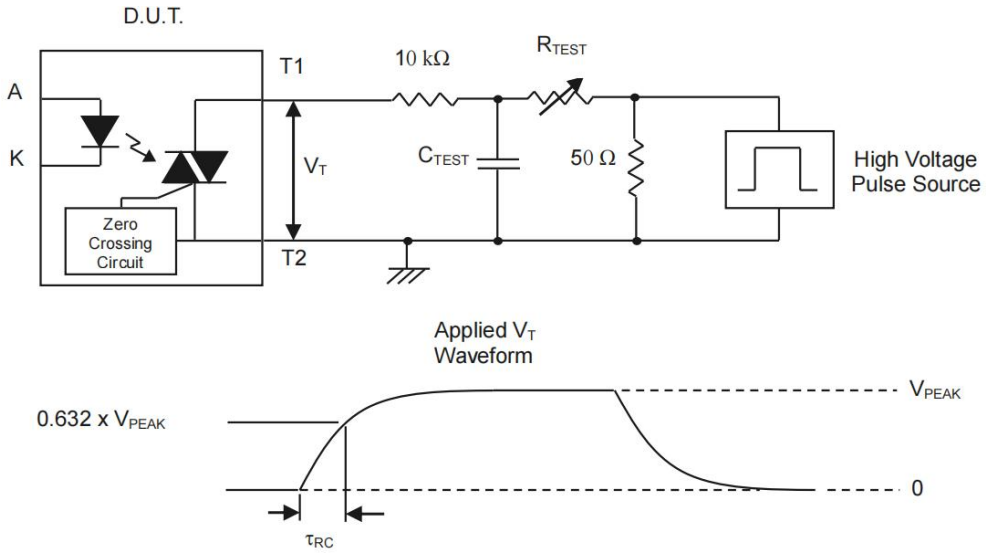


图8.静态dv/dt测试电流和波形 Static dv/dt Test Circuit & Waveform



测量方法 Measurement Method

高压脉冲设置为所需的VPEAK值, 并通过上述RC电路应用于D.U.T输出值，不应用LED电流,使用X100范围探头监视波形VT, 通过改变RTEST, dv/dt(斜率)增加, 直到DU被观察到触发(波形崩溃)。dv/dt随后下降,直到D.U.T. 停止触发, 此时记录 τ_{RC}, 计算dv/dt=0.632*V_{PEAK}/τ_{RC}

The high voltage pulse is set to the required VPEAK value and applied to the D.U.T. output side through the RC circuit above. LED current is not applied. The waveform VT is monitored using a x100 scope probe. By varying RTEST, the dv/dt (slope) is increased, until the D.U.T. is observed to trigger (waveform collapses). The dv/dt is then decreased until the D.U.T. stops triggering. At this point, τ_{RC} is recorded and the dv/dt calculated. dv/dt=0.632*V_{PEAK}/τ_{RC}

例如, KL305X系列的V_{PEAK}=600V, dv/dt值的计算公式如下:

For example, V_{PEAK} = 600V for KL305X series. The dv/dt value is calculated as follows:

dv/dt= 0.632*600/τ_{RC}

8. 订单信息 Order Information

• 材料编号 Part Number

KL305XY-Z-FV-AW

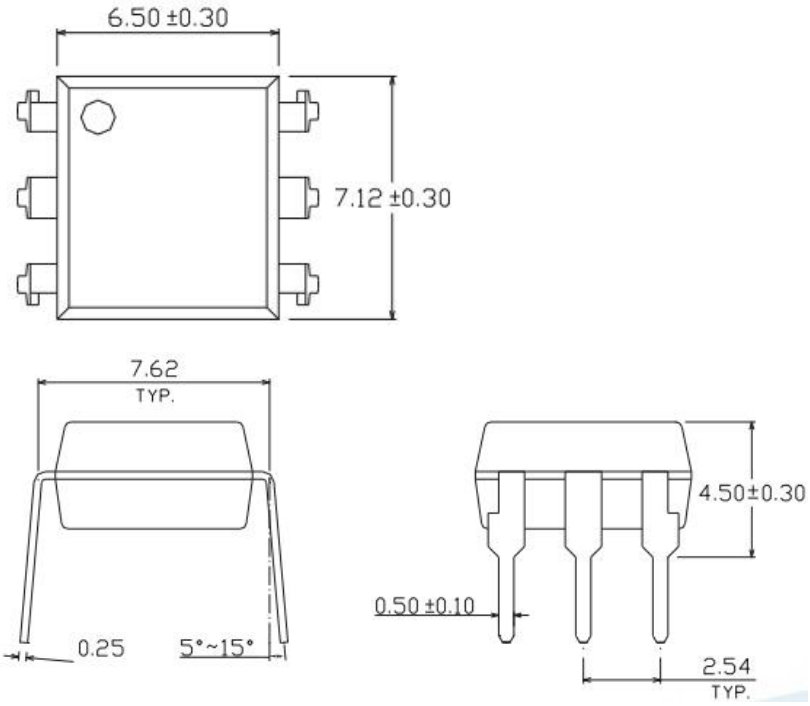
附注(Notes):

X = 零件编号(1、2 或 3) Part No. (1, 2 or 3)
Y = 引脚形式选项(S、S1、M 或 无)
Lead form option (S, S1, M or none)
Z = 料带和卷轴选项(TA、TB 或 无)
Tape and reel option (TA, TB or none)
F = 表示支架框架选项(F:铁材、无:铜材)
Lead frame option (F : Iron , None : copper)
V = 表示VDE标识(客户指定镭射字符才加"V")
VDE (Only add "V" to laser characters specified by the customer)
AW =表示消费级 Consumer-grade

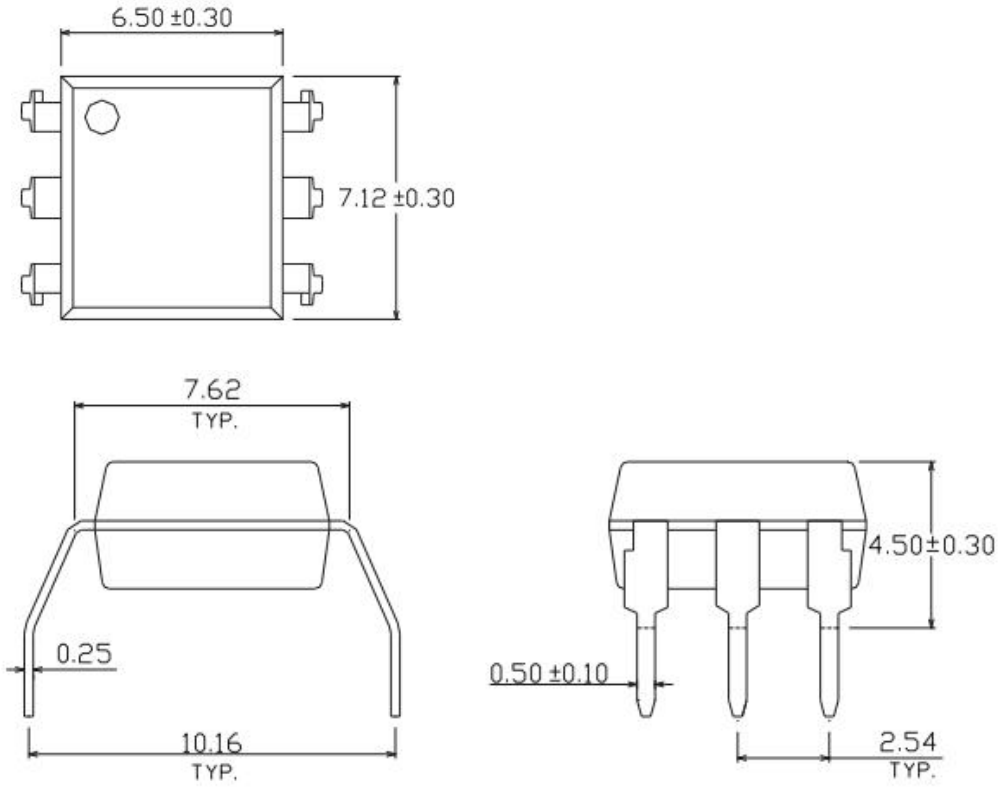
选项 Option	描述 Description	包装数量 Packing quantity
无 None	标准DIP-6 Standard DIP-6	每管65pcs 65 units per tube
M	宽引脚弯曲(0.4英寸间距) Wide lead bend (0.4 inch spacing)	每管65pcs 65 units per tube
S-TA	表面贴装引线形式+TA载带和卷轴选项 Surface mount lead form + TA tape & reel option	每卷1000pcs 1000 units per reel
S-TB	表面贴装引线形式+TB载带和卷轴选项 Surface mount lead form + TB tape & reel option	每卷1000pcs 1000 units per reel
S1-TA	表面贴装引线形式(低剖面)+TA载带和卷轴选项 Surface mount lead form (low profile) + TA tape & reel option	每卷1000pcs 1000 units per reel
S1-TB	表面贴装引线形式(低剖面)+TB载带和卷轴选项 Surface mount lead form (low profile) + TB tape & reel option	每卷1000pcs 1000 units per reel

9. 封装尺寸(单位:毫米) Package Drawing(Unit:mm)

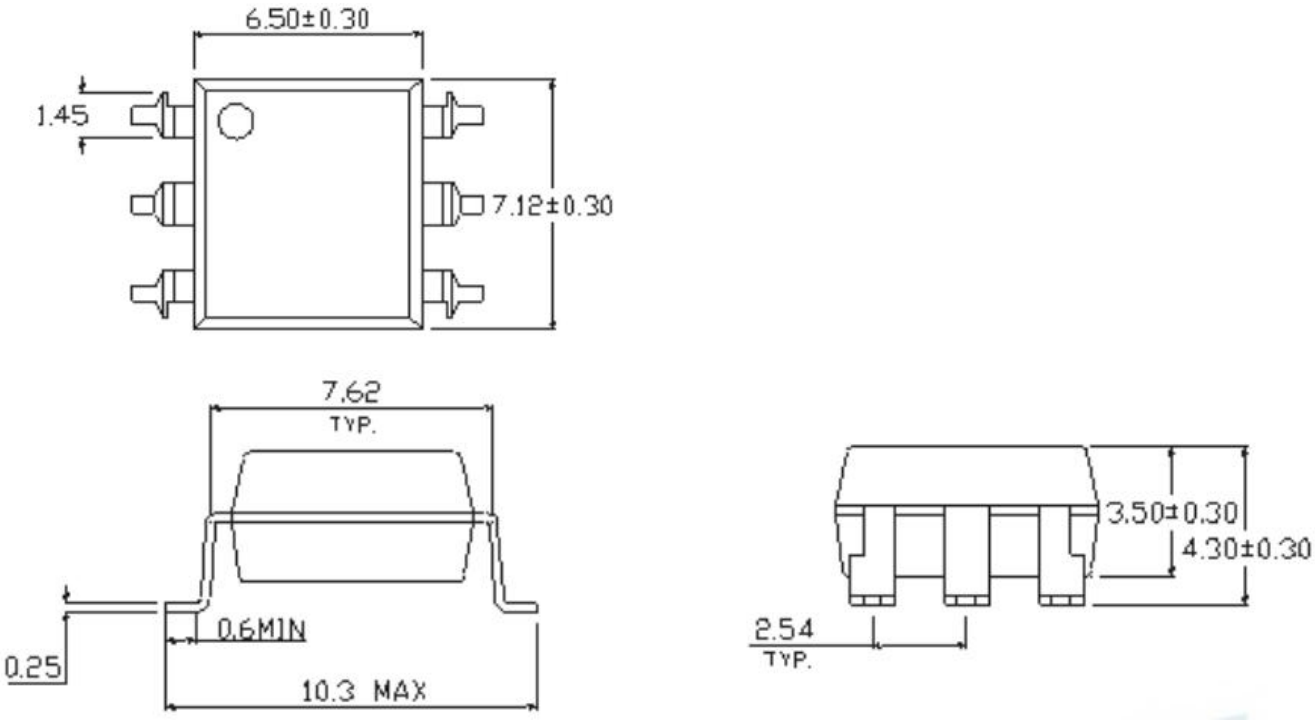
• 标准DIP型号 Standard DIP Type



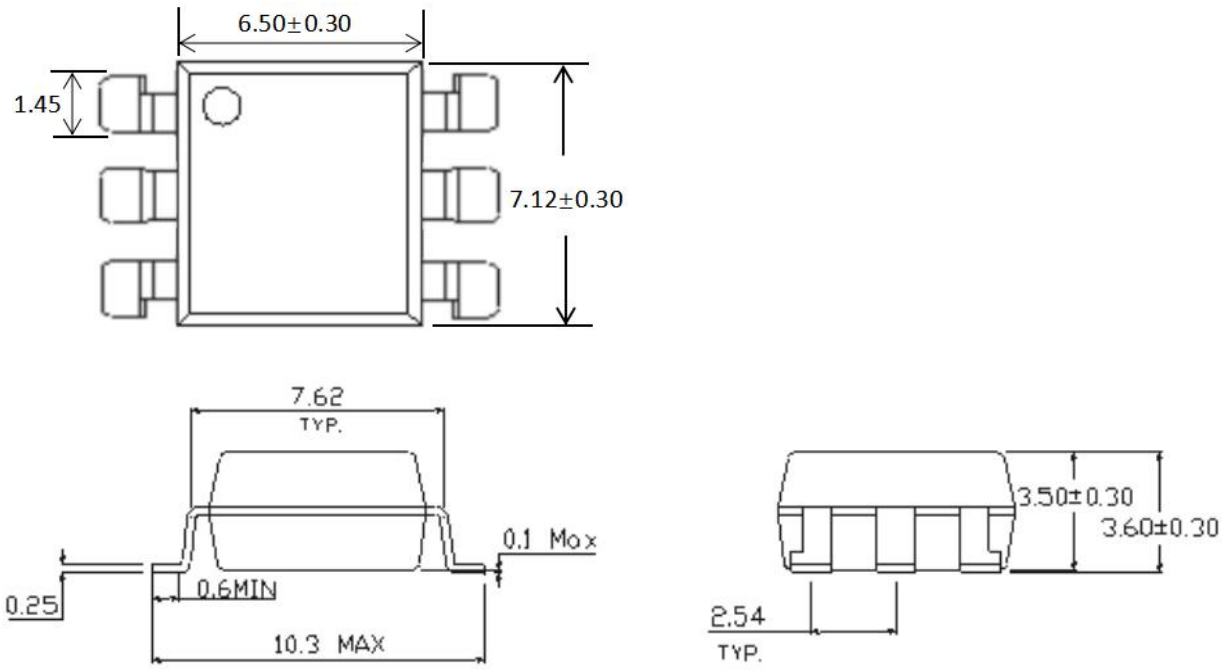
• 选择M型号 Option M Type



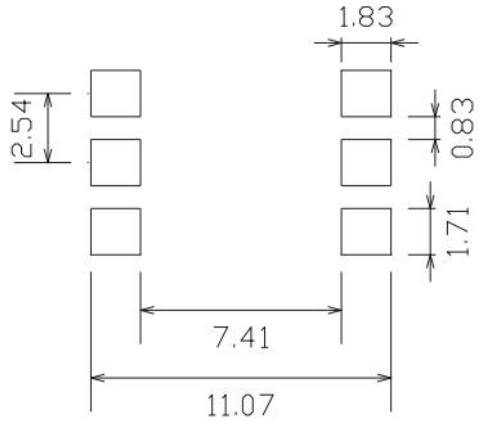
• 选择S型号 Option S Type



• 选择S1型号 Option S1 Type



• 表面贴片类型PIN脚焊盘布局 Surface patch type PIN foot pad layout

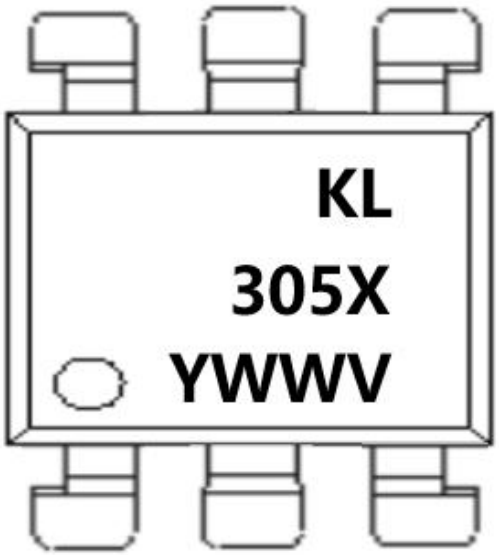


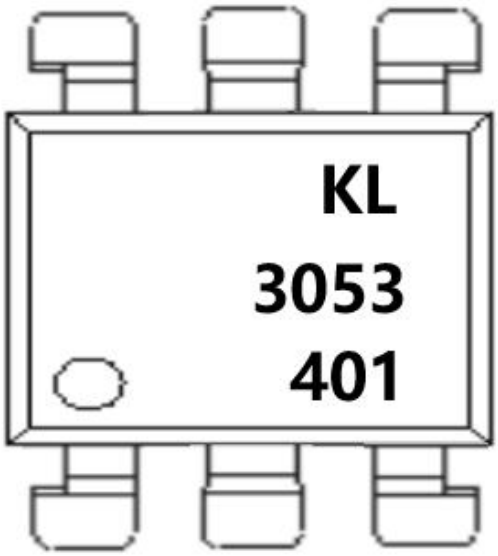
备注 Notes

a. 建议焊盘尺寸仅供参考Suggested pad dimension is just for reference only

b.请根据个人需要修改焊盘尺寸Please modify the pad dimension based on individual needs

10. 设备标记 Device marking





(范本参考 Template reference)

附注(Notes):

KL = 表示晶台微电子有限公司 Denotes Kinglight Microelectronics

305X = 表示材料部件号 Denotes Device Part Number
X表示零件编号(1、2 或 3) Part No. (1, 2 or 3)

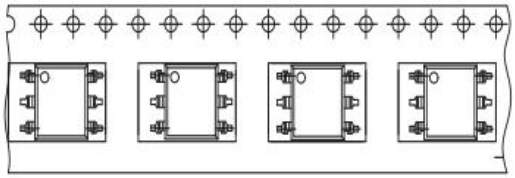
Y = 表示1位年份代码Denotes 1 digit Year code

WW = 表示2位周别代码Denotes 2 digit Week code

V = 表示VDE标识(客户指定镭射字符才加"V")
VDE (Only add "V" to laser characters specified by the customer)

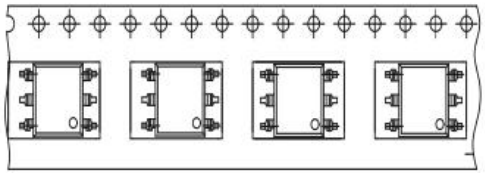
11. 料带和卷轴包装规格 Tape & Reel Packing Specifications

• 选择TA Option TA



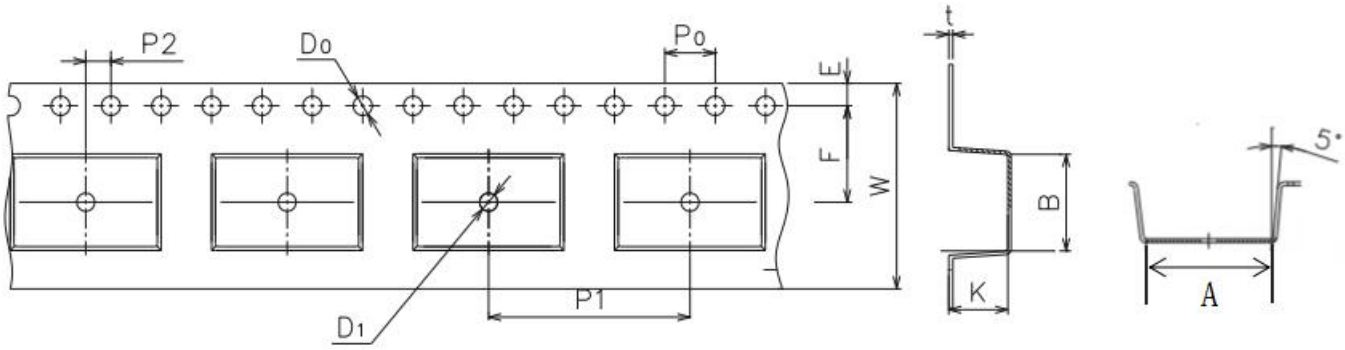
卷轴进给方向 Direction of feed from reel

• 选择TB Option TB



卷轴进给方向 Direction of feed from reel

料带尺寸Material belt size



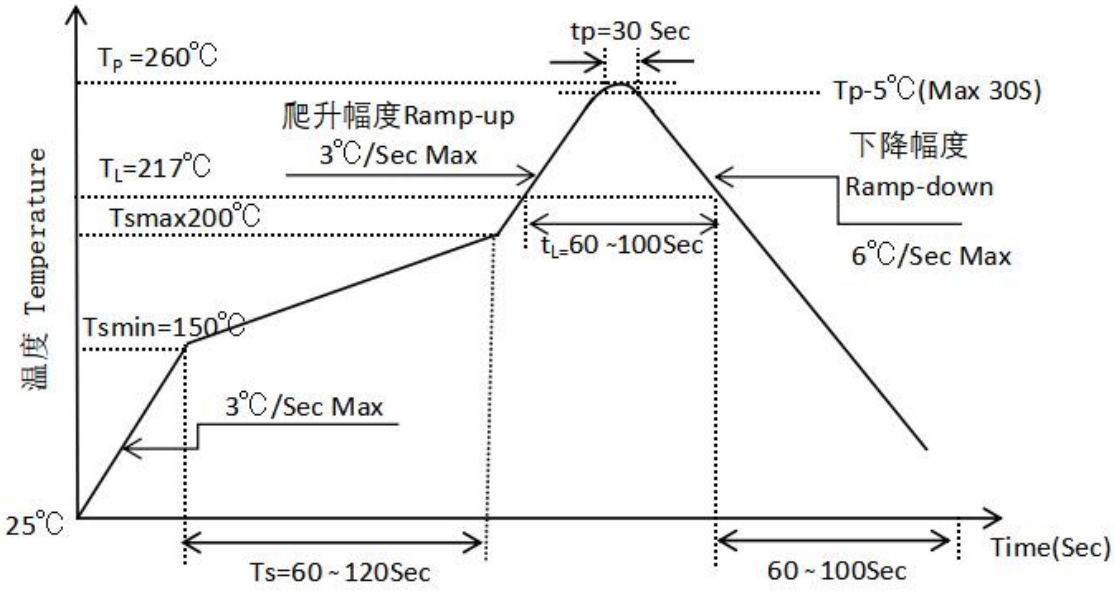
尺寸编号 Dimension No.	A	B	D0	D1	E	F
尺寸(mm) Dimension(mm)	10.8±0.1	7.5±0.1	1.5±0.1	1.5±0.1	1.75±0.1	7.5±0.1
尺寸编号 Dimension No.	P0	P1	P2	t	W	K
尺寸(mm) Dimension(mm)	4.0±0.15	16.0±0.1	2.0±0.1	0.35±0.03	16.0±0.2	4.5±0.1

12. 焊接温度曲线 Temperature Profile Of Soldering

• 回流焊温度曲线 Reflow soldering

建议在下面所示的温度和时间分布条件下, 进行一次回流焊作业, 不得超过三次

One time soldering reflow is recommended within the condition of temperature and time profile shown below. Do not solder more than three times.

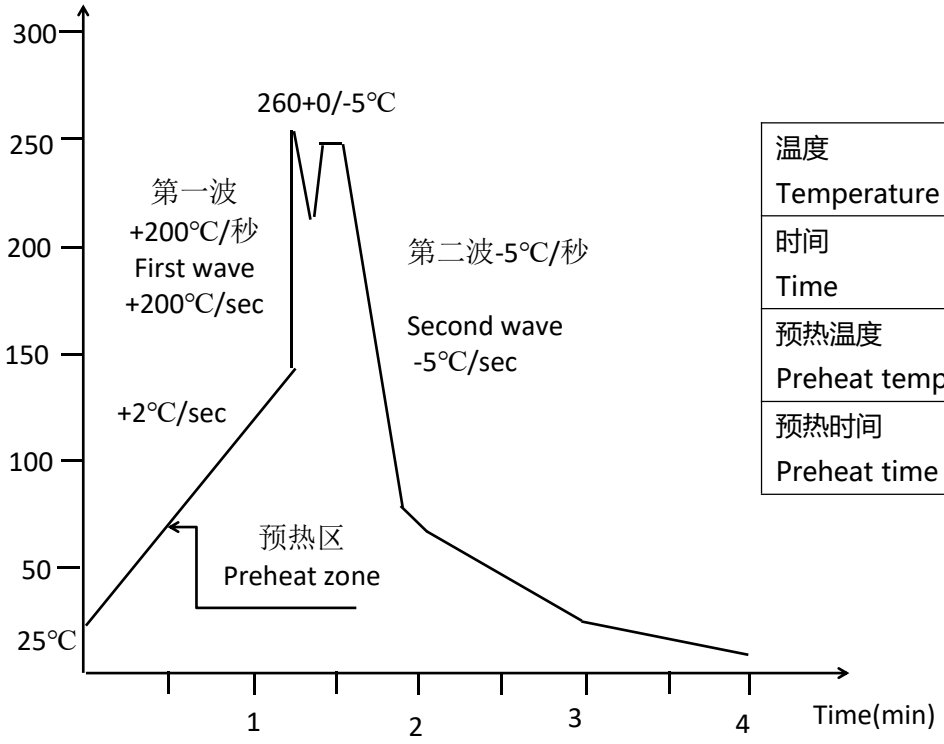


项目 Item	符号 Symbol	最小值 Min.	最大值 Max.	单位 Unit
预热温度 Preheat Temperature	T_s	150	200	℃
预热时间 Preheat Time	ts	60	120	s
升温速率 Ramp-Up Rate (T_L to T_P)	-	-	3	℃/s
液相线温度 Liquidus Temperature	T_L	217		℃
高于液相线温度(T_L)的时间 Time above Liquidus Temperature T_L	t_L	60	100	s
峰值温度 Peak Temperature	T_P	-	260	℃
T_c 在(T_P-5)和 T_P之间的时间 Time During Which T_c Is Between (T_P-5) and T_P	t_p	-	30	s
降温速率 Ramp-down Rate(T_P to T_L)	-	-	6	℃/s

• 波峰焊温度曲线 Wave Soldering

温度条件下, 建议一次焊接

One time soldering is recommended within the condition of temperature



温度 Temperature	260℃+0/-5℃
时间 Time	10秒 10S
预热温度 Preheat temperature	25至140℃ 25 to 140℃
预热时间 Preheat time	30至80秒 30 to 80 S